

FORM PTO-1449	Atty. Docket No.: R11.12-0856	Appl. No.: 10/825,475
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor:	
	Garrie D. Huisenga	
	Filing Date	Group Art:
	April 15, 2004	2836

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class.	Sub Class	Filing Date If Appropriate
MN	AA	4,206,297	06/1980	Dahlke	323	22
	AB	4,502,318	03/1985	Converse III et al.	73	3
	AC	4,621,551	11/1986	Silverman	82	31
	AD	4,755,773	07/1988	Ohmagari	332	9
	AE	4,804,958	02/1989	Longsdorf	340	860
	AF	4,833,922	05/1989	Frick et al.	73	756
	AG	4,866,435	09/1989	Frick	340	870.16
	AH	4,889,179	12/1989	Merenda	165	14
	AI	5,087,871	02/1992	Losel	323	299
	AJ	5,089,974	02/1992	Demeyer et al.	364	492
MN	AK	5,146,401	09/1992	Bansal et al.	364	138

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Sub Class	Translation Yes No
MN	AL	WO94/01764	01/1994	EPO			
MN	AM	4021258A1	01/1991	Germany			
MN	AN	2229897A	03/1990	United Kingdom			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MN	AO	Brochure: "Preliminary LTC1149, LTC1149-3.3., LTC 1149-5; High Efficiency Synchronous Stepdown Switching Regulator", Linear Technology Corp. Milpitas, CA, pp. 1-16, Nov. 1992.
MN	AP	Ullrich Von Seckendorff and Karen Speers, "Selecting an Inrush Current Limiter," Electronic Products, Jan. 1995, pp. 25-33.
MN	AQ	"Electronics Letters an International Publication," The Institution of Electrical Engineers, August 29, 1996, vol. 32, No. 18, pp. 1629-1632.
EXAMINER: Matthew V. Nguyen		DATE CONSIDERED: 3/27/06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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MN	AR	5,179,488	01/1993	Rovner	361	18
	AS	5,187,474	02/1993	Kielb et al.	340	870
	AT	5,245,333	09/1993	Anderson et al.	340	870
	AU	5,258,868	11/1993	Jensen et al.	359	168
	AV	5,333,114	07/1994	Warrior et al.	364	550
	AW	5,606,513	02/1997	Louwagie et al.	364	510
MN	AX	5,973,942	10/1999	Nelson et al.	636	21.01
	AY					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AZ						
BA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MN	BB	"LM193/LM293/LM393/LM2903 Low Power Low Offset Voltage Dual Comparators", National Semiconductor, pp. 1-12, January 1995.
MN	BC	"LT124/Series High Speed Current Mode Pulse Width Modulators," Linear Technology LT1241 Series, pp. 1-16, 1992.
MN	BD	A.S. Koslovski, "Fast Active Inrush Current Limiter for Boost-based Resistor Emulators," 16th International Telecommunications Energy Conference, Intelec '94, Vancouver, B.C., Canada, pp. 649-652, October 30, 1994.

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FORM PTO-1449 <div style="text-align: center;"> LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT </div> <div style="text-align: center; margin-top: 20px;">  </div>	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td colspan="2">Atty. Docket No.: R11.12-0856</td> <td>Appl. No.: 10/825,475</td> </tr> <tr> <td colspan="3" style="height: 40px; vertical-align: top;">First Named Inventor:</td> </tr> <tr> <td colspan="3">Garrie D. Huisenga</td> </tr> <tr> <td style="width: 50%;">Filing Date</td> <td colspan="2">Group Art:</td> </tr> <tr> <td>April 15, 2004</td> <td colspan="2">2836</td> </tr> </table>	Atty. Docket No.: R11.12-0856		Appl. No.: 10/825,475	First Named Inventor:			Garrie D. Huisenga			Filing Date	Group Art:		April 15, 2004	2836	
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AA						
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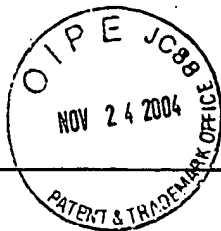
FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AL						
AM						
AN						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MN	AO	Brochure: "LM494, Pulse Width Modulated Control Circuit", National Semiconductor, 7 pages, June 1989
MN	AP	Brochure: "High-Efficiency, +5V Adjustable Step-Down Switching Regulator", Maxim Integrated Products, 8 pages, December 1991
	AQ	
EXAMINER:		<div style="display: flex; justify-content: space-between;"> <i>Matthew V. Nguyen</i> DATE CONSIDERED: 3/27/06 </div>

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Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
MN	AA	5,535,243	7/1996	Voegele et al.	375	259
	AB	4,015,609	4/1977	Mensink et al.	128	419 PG
	AC	4,413,224	11/1983	Krupka et al.	323	222
	AD	4,606,076	8/1986	Davis	455	343
	AE	4,970,451	11/1990	Suomalainen	323	222
	AF	5,184,094	2/1993	Kohler	331	158
	AG	5,360,997	11/1994	Watson	323	268
	AH	5,377,090	12/1994	Steigerwald	363	20
	AI	5,541,860	7/1996	Takei et al.	702	141
	AJ	5,610,552	3/1997	Schlesinger et al.	327	560
	AK	5,659,206	8/1997	Taguchi et al.	307	11
	AL	5,710,552	1/1998	McCoy et al.	340	870.21
	AM	5,764,891	6/1998	Warrior	395	200.2
	AN	5,973,942	10/1999	Nelson et al.	363	21
MN	AO	6,813,318	11/2/2004	Westfield et al.	375	295

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
MN	AP	0 529 391 A1	6/1998	EP		X
	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)					
	AQ	MAXIM Data sheet for MAX 631/632/633 CMOS Fixed/Adjustable Output Step-Up Switching Regulators (03/90)				
	AR	Article: "Getting Maximum Efficiency from a 4-20 mA transducer Power Supply," by Bernhard Konrad of Maxim Integrated Products, Sensors, pp. 74-75, April 1996.				
	AS	Schematic: Power supply circuited used in a process control transmitter which is believed to have been on sale more than one year prior to the filing of the present application.				
MN	AT	Schematic: Power supply circuited used in a process control transmitter which is believed to have been on sale more than one year prior to the filing of the present application.				

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